501.43170X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: FUKUI, et al.

Serial No.: Not yet assigned

Filed: October 2, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Mail Stop DD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 2, 2003

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

Although some of the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by an English language version of a patent family member; English language abstract or the discussion of the documents in the specification.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case: 501.43170X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

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Sheet 1 of 1

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.43170X00	SERIAL NO.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT FUKUI, et al.	
		FILING DATE October 2, 2003	GROUP

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Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
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	AB	5,410,507	04/25/1995	TAZUNOKI, et al.	365	189.09	04/25/1995
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	AN	10-74394	03/17/1998	JP	G11C	11/413	X	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AU	HORIGUCHI, et al., "A Tunable CMOS-DRAM Voltage Limiter with Stabilized Feedback Amplifier", IEEE Journal of Solid-State Circuits, Vol. 25, October 1990, Pgs. 1129-1135
	AV	
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	AZ	

Examiner	Date Considered

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